

We claim:

1. An electronic component, comprising:

a housing having a first side face; and

at least two terminal pins protruding out of said first side face of said housing;

said housing having an isolation barrier formed between said two terminal pins.

2. The component according to claim 1, wherein said isolation barrier is integrally formed onto said housing.

3. The component according to claim 2, wherein:

said first side face has a first edge and a second edge opposite said first edge; and

said isolation barrier is a plate extending on said first side face from said first edge to said second edge.

4. The component according to claim 1, wherein:

said first side face has a first edge and a second edge opposite said first edge; and

said isolation barrier is a plate extending on said first side face from said first edge to said second edge.

5. The component according to claim 1, wherein:

said isolation barrier has a side remote from said first side face of said housing; and

said two terminal pins protrude beyond said side of said isolation barrier.

6. The component according to claim 1, wherein said housing has at least one side wall that together with said isolation barrier encloses said two terminal pins from at least three sides.

7. The component according to claim 1, wherein said at least two terminal pins protrude out of said housing next to each other in a row.

8. The component according to claim 1, wherein:

said first side face has a first edge and a second edge opposite said first edge;

a first one of said two terminal pins is located closer to said first edge than a second one of said two terminal pins; and

said second one of said two terminal pins is located closer to said second edge than said first one of said two terminal pins.

9. The component according to claim 1, comprising:

a plurality of isolation barriers that includes said isolation barrier;

said housing having side walls that together with said plurality of said isolation barriers surrounds at least one of said two terminal pins from four sides.

10. A transistor, comprising:

a housing having a first side face; and

at least two terminal pins protruding out of said first side face of said housing;

said housing having an isolation barrier formed between said two terminal pins.